



*Drawing Done
Approved
10/14/03 CEE*

Attorney's Docket No.: 10559-108001 / P7643
Assignee: Intel Corporation

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9-8-03*

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Keith Dow Art Unit : 2181
Serial No.: 09/461,643 Examiner : Christopher E. Lee
Filed: December 14, 1999
Title: IMPROVED SIGNAL ROUTING BETWEEN A MEMORY CONTROL
UNIT AND A MEMORY DEVICE

Attention: Official Draftsman
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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Technology Center 2100

TRANSMITTAL OF FORMAL DRAWINGS

Please substitute the enclosed 3 sheets of formal drawings
for the corresponding drawings presently in the application.

Please apply any charges or credits to Deposit Account
No. 06-1050.

Respectfully submitted,

Date: 8/25/03

[Signature]
John F. Conroy
Reg. No. 45,485

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CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being
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